ECE 544 Final Project Report

**Sunlight Tracking System**

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**Project Description:**

The primary objective of the project was to create a system capable of tracking the light source in 2 axes. The main use case for this system will be for a simple solar tracker for solar panels to point to the direction of the sun to maximize energy production. There by increasing the efficiency of the system.

**Design details**:

The project was split into three smaller sub sections making it easier to design and make progress.

The Base project consist of:

* A Light Dependent Resistor (LDR) based sensor array to find which quadrant receives more light.
* Read the analog sensor reading using xADC or external ADC
* Design the control system using an FPGA
* 2 axis motion is achieved using 2 hobby servo motors
* Able to set and reset the position using encoder, switches and push button
* Display the current position values and debug parameters on the OLED display

Diagram

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Fig1: Base project System Block Diagram

The extra addons to the base project is split into two sections too:

Part 1:

* Build the solar panel and battery charging system.
* Use the solar panel itself to track the movement of light source

Part 2:

* Logging the energy production data and sending the data to the cloud using Blynk IO
* Making a simple prototype android app display the reading using Blynk IO

**Theory of operation**:

Hardware:

The core of the project is getting the tracking information from the sensor unit, processing it and generating the command signals, PWM signals in this case to command the actuator to move and point the end effector to the light source.

FPGA Hardware implementation:

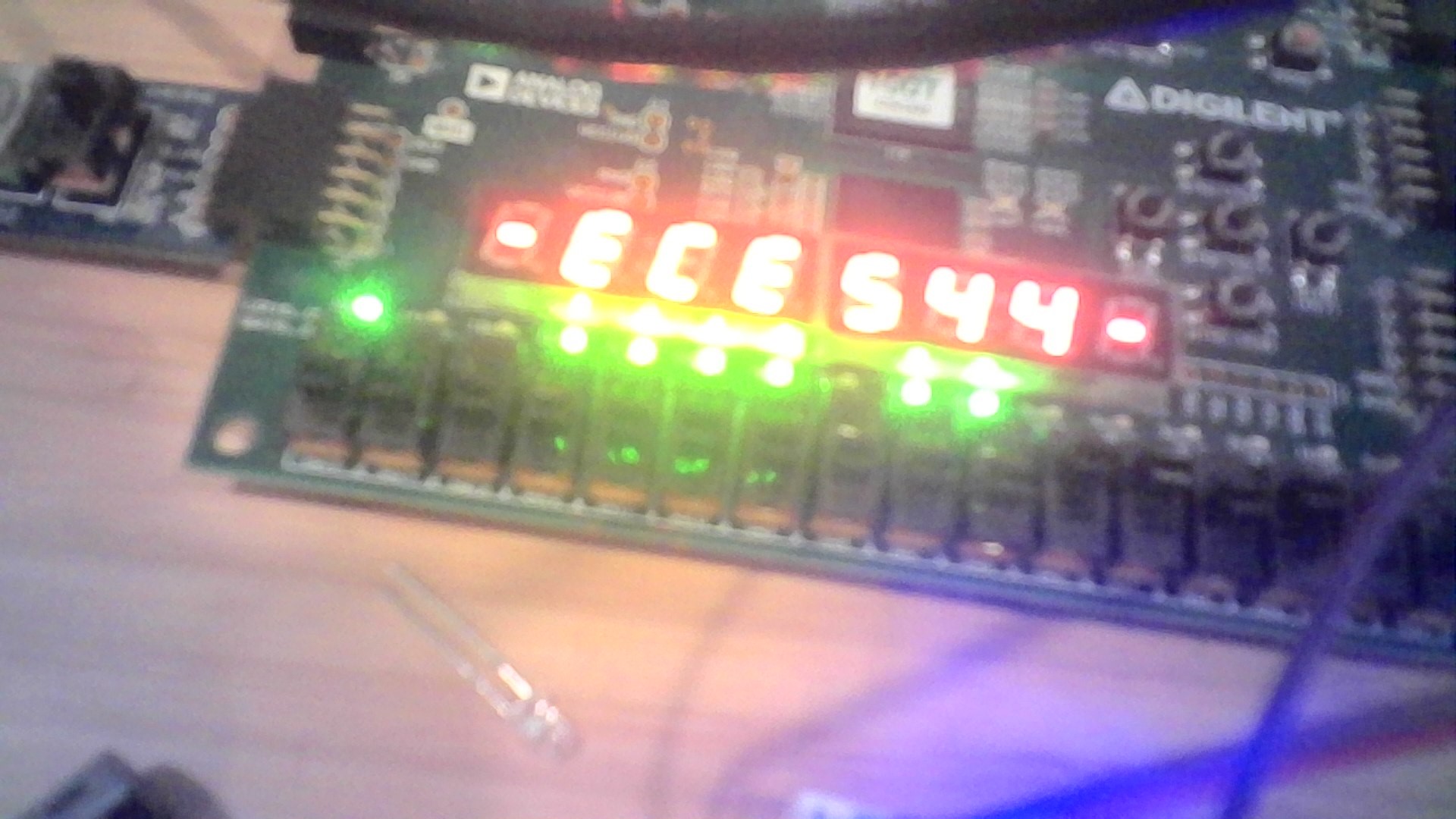
The hardware implementation block diagram is included in the <file name> . In this section, we will briefly describe the blocks that are being used and their functionality. The System is developed around the microblze softcore, and the implementation is done based on the getting started project provided in the course.

xADC IP block:

Diagram, schematic

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Neima was able to implement the XADC. He made a connection between the Digital Output of the XADC and the 16 LEDs of the FPGA directly in verilog so the digital signal did not need to be transferred to the code to demonstrate an effect. When we connected 4 analog pmod inputs of the Nexy A7, we were able to use switches and our C code to change which of the 4 pins were having their data converted by the XADC. We knew this was happening because the LEDS changed to reflect the voltage we were putting across different pins. However, despite the visible GPIO connections to the ADC output and our ability to change the active channel of the XADC using the other GPIO, we were not able to get our C code to successfully read the digital signal off the XADC. We were however able to get the LEDS to change based on those verilog connections.



PWM generator IP block:

Neima was able to implement the AXI PWM block in a very straightforward manner and we were able to successfully control it through our C code. We initially tested that this worked by hooking the PMOD pins we channeled the PWM through to LEDs.

Diagram, schematic

Description automatically generated

**Mechanical designs**

Actuation:

In order to achieve the pan and tilt motion, we decide to use servo motors connect to mechanical parts that can achieve the actuation. Instead of designing the mechanical parts from scratch, we decided to use Pan and Tilt mechanical structures designed by the opensource community. We were able to find a design that suits our specification on thingiverse.com. <https://www.thingiverse.com/thing:2712918>

This helped us save some time in the design process. We didn’t have to use all the parts of the project however some of the parts were used. The Pan and tilt model would look like as show in the example model.

A picture containing metalware

Description automatically generatedA picture containing metalware

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Fig 2: 3D model for the Pan and Tilt unit

Servo motors:

We decide to use MG996R Servos that got metal gears and offer sufficient torque for the operation of the system.

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Fig3: Servo motor

These servos are controlled my sending PWM signals with a period of 20ms and duty ranging 2.5% to 11%. The angel of rotation is mapped such that lower duty corresponds to 0 0 and higher duty corresponds to 11% duty.

LDR Sensor Unit:

The sensor unit using potentiometers connect in a voltage divider network as shown in the schematic below. R1, R4, R7, R10 resistors were used to limit the upper bound of the output voltage. Here output voltage of each sensor network increases as the intensity of the light falling on the LDR increases and vice versa.

Diagram, schematic

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Fig 4: Sensor network

A picture containing weapon

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Fig 5: LDR Sensor Unit

Overall test setup:

A picture containing electronics, circuit

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Solar unit:

Since we were running out of time, we could build only a simple 10V panel made of 5V 30mA min solar cells. An MPTT HiLetgo CN3791 Solar Charge Controller Board is used to charge a 3.7V 2000mA LiPo battery.

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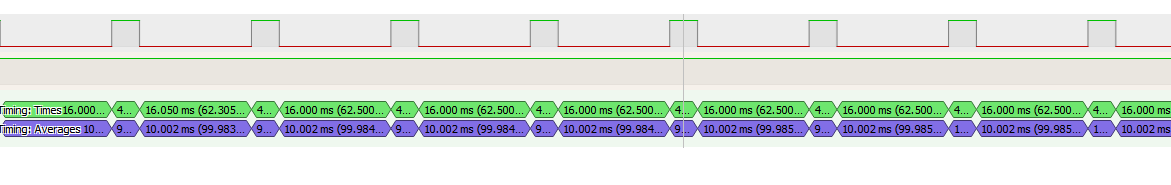
Firmware development:

At the firmware side, we stuckto the standalone OS, build on top of the starter code provide in the course. On breaking down the firmware into sections, we have the following blocks.

PWM generation:

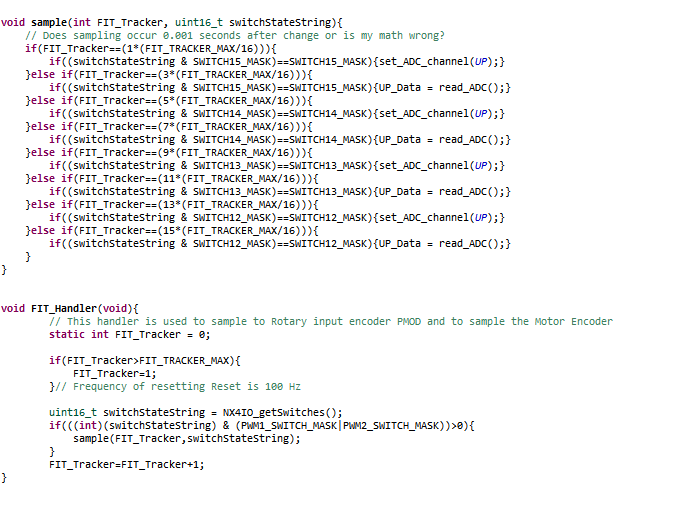
The LightFollow() function controlled our main loop and was the part of the software that controlled the OLED Screen and told the 2 channels of the PWM Generator in the FPGA what Period and Duty Cycle to use. Through experimentation we were able to find that the PWM wave needed to have a Period of 20 milliseconds and that the duty cycle needed to be 2.5% to move to 0 degrees and about 11% to move to 180 degrees.

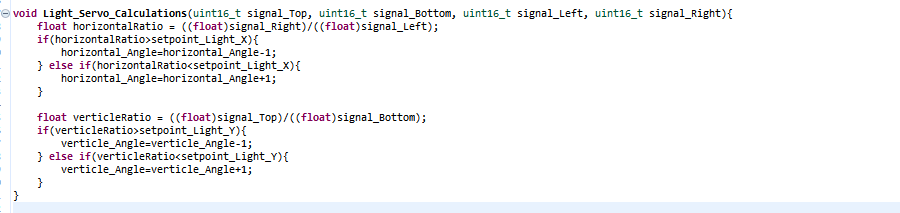
The initial PWM duty cycle that Light Follow started the PWM at was 4 milliseconds and we were able to confirm that we could successfully get that and other PWM duty cycles using a oscilloscope and logic analyzer. We used Pulseview with a logic analyzer and the results can be seen in the following picture.



Senor reading:

The Sampling of the signals from the photoresistors and the control of the XADC Analogue to Digital Converter were done in the FIT interrupt. This was done to ensure that these sections of the code ran regardless of whether the main loop was busy with moving the servos or with writing to the OLED. By controlling the ADC through the interrupts, we were able to ensure more consistent timing from them. Unfortunately our C code was not able to successfully read digital signals from the GPIO we connected to that output despite the fact that we were able to control the channels of the ADC with another GPIO. We are still debugging why.





It was in this FightFollow() function that we called the Light\_Servo\_Calculations() function. This function would use the Photoresistor readings after the digital conversion to determine what direction the Servos needed to move. This function served as our control algorithm and was intended to take in the digital signals held in global variables that were updated by the FIT command.

Results:

By time of the demo night, we were only able to get the servos working, change the PWM parameters using switches, push buttons and encoder, and display the parameters on the oLED Pmod. Our power source was having trouble in proving sufficient power to the servos and sometimes the servo motors go haywire started rotating continuously.

Good stuff-

* We got the bare minimum hardware setup ready for testing.
* Was able to successfully generate proper PWM signals using the FPGA and command the servos to reach the target position.
* We could get the xADC implemented in the standalone project provided by diligent

Challenges:

* We had a late start for the project that caused issues in the overall schedule.
* Working remotely from two different locations and getting in sync with the project wad hard too.
* Had a tough time managing work and other academic schedule and put in the extra time we had lost due to the late start
* Shipping issue with the parts we ordered online, it arrived only on the day
* The servo motors started going haywire, turns out it was caused by faulty power supply that could not give sufficient inrush current demand.
* Had a lot of trouble in getting the xADC working properly.
* We ran out to time to go with the back up option of reading the analog signal values using external ADC.

What we would have done differently?

The heart of the project was to get the tracking data into the system so that we can generate the position commands to the servos. However, In our case, we ran out of time to get the xADC working correctly when we tried to interface it with the whole design.

* First thing we should have done was to stop working on getting the xADC working and get an alternate way of getting the data into the system by using a Pmod or external ADC or ever using another development board such an Arduino. We can then communicate with the external ADC via SPI, I2C or UART protocol to get the data into the system.
* The additional hardware we ordered took more time to arrive and we didn’t have the time to interface it with our system. We should have planned for a case were what if we are unable to get the xADC working, foreseeing a backup option. We have received a separate ADC now which should be able to communicate with the FPGA through I2C.

**Contributions:**

Neima:

* FPGA hardware implementation and Verilog code
* Firmware development and testing in C
* Documentation

Abhijeet:

* Worked on the electromechanical design of the system.
* Worked on designing the solar charger system.
* Testing the firmware on the hardware system.
* Preparing presentation and documentation

**Appendix:**

**Code Explanation:**

mainPhotoServo.c is the file with our main program in it. The movement feedback system is not necessarily called correctly as we were not able to get the test data needed to refine that feedback loop.

PWMTester.c is an earlier version of the code we used to test functionality of the PWM and ADC.

Git Repository: https://github.com/abhijeet8prem/ECE-544-final-project

Video Demo Playlist link: <https://youtube.com/playlist?list=PLsCIdOxzoIHiMhNfHu9gVXczjME57Xzb0>

Reference projects:

<https://www.youtube.com/watch?v=dkV3xCrZAqc>

<https://www.youtube.com/watch?v=_6QIutZfsFs&ab_channel=GreatScott%21>

Reading analog data in FPGA:

<https://digilent.com/reference/learn/programmable-logic/tutorials/nexys-4-xadc-demo/start>